



SEP 03 2004

TRANSMITTAL OF FORMAL DRAWINGS

Docket No.
JP920000229US1

In Re Application Of:

Fujio et al

Application No.	Filing Date	Confirmation No.	Examiner	Customer No.	Group Art Unit
09/682,131	07/25/2001	2739	Chris C. Chu	24241	2815

Invention: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE WITH EMI PREVENTION STRUCTURE

Address to:

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Transmitted herewith are:

7 sheets of formal drawing(s) for this application.

Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c).

Signature

Anthony J. Canale, Reg. No. 51,526
IBM Corporation
Intellectual Property Law Dept., 972E
1000 River Street
Essex Junction, VT 05452

Dated: 09/01/2004

I certify that this document and attached formal drawings are being deposited on Sept 1, 2004 with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Signature of Person Mailing Correspondence

ARNETTE DODGE

Typed or Printed Name of Person Mailing Correspondence